

SEMITRANS[®] 3

IGBT4 Modules

SKM600GB12E4

Features*

- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- · Increased power cycling capability
- · With integrated gate resistor
- For higher switching frequencies up to 12kHz

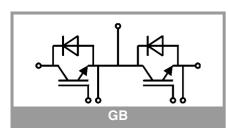
UL recognized, file no. E63532

Typical Applications

- AC inverter drives
- UPS
- Electronic welders

Remarks

• Case temperature limited to $T_c = 125^{\circ}C$ max, recomm. $T_{op} = -40 \dots +150^{\circ}C$, product rel. results valid for $T_j = 150^{\circ}C$



Absolute Maximum Ratings								
Symbol	Conditions		Values	Unit				
IGBT								
V _{CES}	T _j = 25 °C		1200	V				
lc	T _j = 175 °C	T _c = 25 °C	860	Α				
		T _c = 80 °C	702	Α				
I _{Cnom}			600	Α				
I _{CRM}			1800	А				
V _{GES}			-20 20	V				
t _{psc}	$V_{CC} = 800 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1200 V$	T _j = 150 °C	10	μs				
Tj			-40 175	°C				
Inverse d	liode							
V _{RRM}	T _j = 25 °C		1200	V				
I _F	T _j = 175 °C	T _c = 25 °C	623	A				
		T _c = 80 °C	466	А				
I _{Fnom}			500	А				
I _{FRM}			1200	A				
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		2736					
Tj			-40 175	°C				
Module				•				
I _{t(RMS)}			500					
T _{stg}	module without	TIM	-40 125	°C				
V _{isol}	AC sinus 50 Hz	t = 1 min	4000	V				

Characteristics Symbol Conditions Unit min. typ. max. IGBT $I_{\rm C} = 600 \, {\rm A}$ T_i = 25 °C V V_{CE(sat)} 1.80 2.05 V_{GE} = 15 V T_i = 150 °C 2.42 V 2.20 chiplevel V_{CE0} T_i = 25 °C 0.80 0.90 V chiplevel T_i = 150 °C 0.70 0.80 ٧ T_i = 25 °C 1.67 1.92 mΩ V_{GE} = 15 V r_{CE} chiplevel T_i = 150 °C 2.5 2.7 mΩ V 5 V_{GE(th)} $V_{GE}=V_{CE}$, $I_C = 24 \text{ mA}$ 5.8 6.5 ICES $V_{GE} = 0 V, V_{CE} = 1200 V, T_j = 25 °C$ 5 mΑ f = 1 MHz Cies 37.2 nF V_{CE} = 25 V Coes f = 1 MHz2.32 nF $V_{GE} = 0 V$ f = 1 MHz2.04 nF Cres V_{GE} = - 8 V...+ 15 V Q_{G} 3400 nC T_i = 25 °C R_{Gint} 1.3 Ω V_{CC} = 600 V T_i = 150 °C 156 ns t_{d(on)} $I_{\rm C} = 600 \ {\rm A}$ T_i = 150 °C 68 tr ns V_{GE} = +15/-15 V T_j = 150 °C Eon 30 m.J $R_{G \text{ on}} = 1.8 \Omega$ T_i = 150 °C 522 $R_{G off} = 1 \Omega$ ns t_{d(off)} $di/dt_{on} = 9100 \text{ A}/\mu \text{s} T_{i} = 150 \text{ °C}$ 138 tf ns di/dt_{off} = 4000 A/µs dv/dt = 3500 V/µs T_i = 150 °C 77 $\mathsf{E}_{\mathsf{off}}$ mJ L_s = 25 nH R_{th(j-c)} per IGBT 0.049 K/W K/W per IGBT (λ_{grease} =0.81 W/(m*K)) 0.032 R_{th(c-s)}



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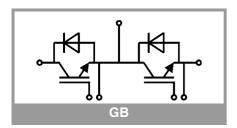
Typical Applications

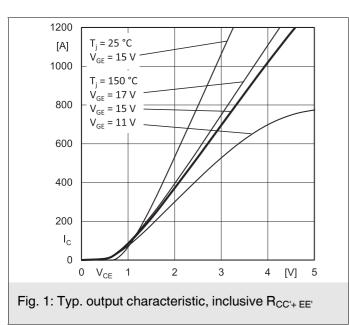
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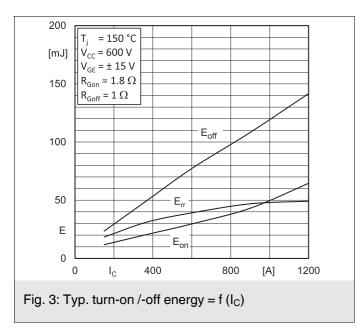
Remarks

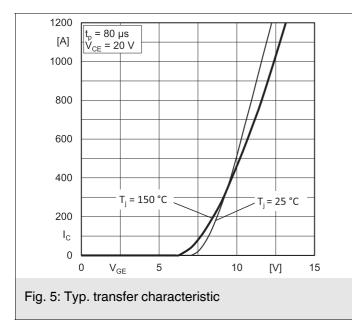
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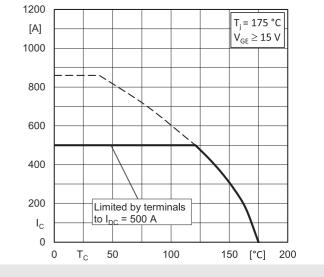
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					
$V_F = V_{EC}$	$I_{\rm F} = 600 {\rm A}$	T _j = 25 °C		2.28	2.63	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.28	2.61	V
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
r _F	chiplevel	T _j = 25 °C		1.64	1.88	mΩ
		T _j = 150 °C		2.3	2.5	mΩ
I _{RRM}	$I_{F} = 600 \text{ A} \\ di/dt_{off} = 8500 \text{ A}/\mu\text{s} \\ V_{GE} = -15 \text{ V} \\ V_{CC} = 600 \text{ V} $	T _j = 150 °C		559		Α
Q _{rr}		T _j = 150 °C		98		μC
E _{rr}		T _j = 150 °C		39		mJ
R _{th(j-c)}	per diode			0.095	K/W	
R _{th(c-s)}	per diode ($\lambda_{grease}=0$		0.039		K/W	
Module	÷					
L _{CE}				15		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ
		T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without t (λ _{grease} =0.81 W/(m*		0.00879		K/W	
R _{th(c-s)2}	including thermal co Ts underneath mod $(\lambda_{grease}=0.81 \text{ W/(m}^*)$		0.014		K/W	
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
			1	-		Nm
w			1		325	g

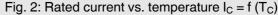


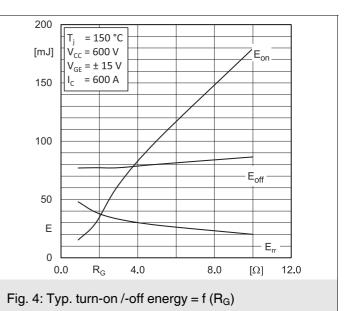


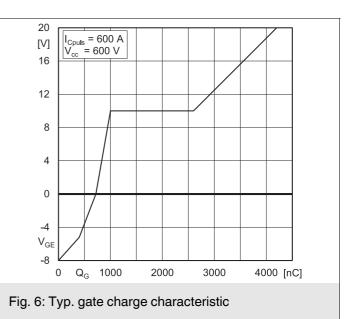


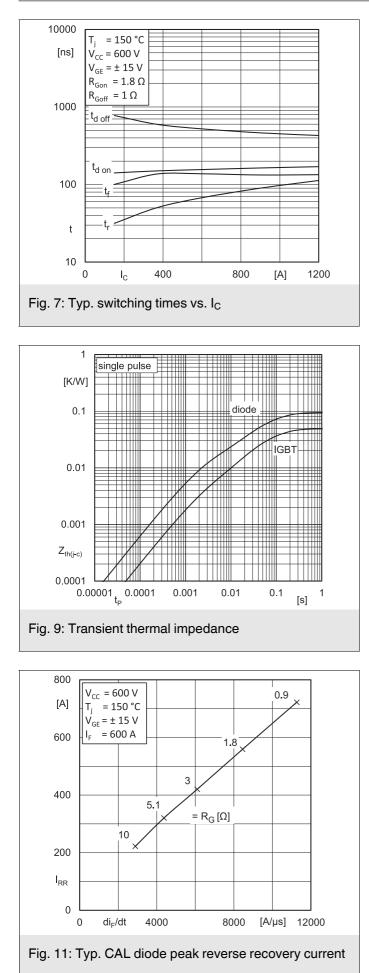












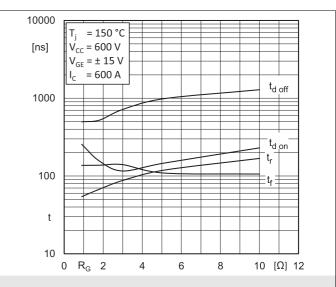
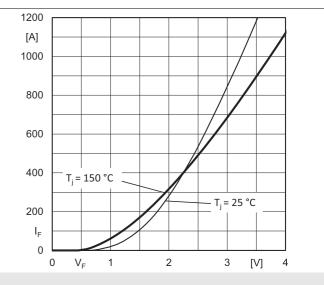
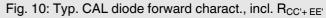
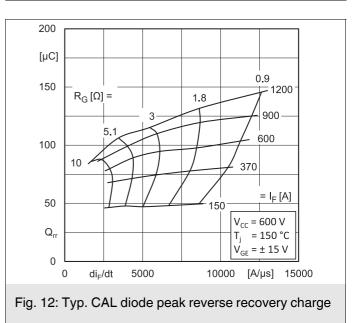
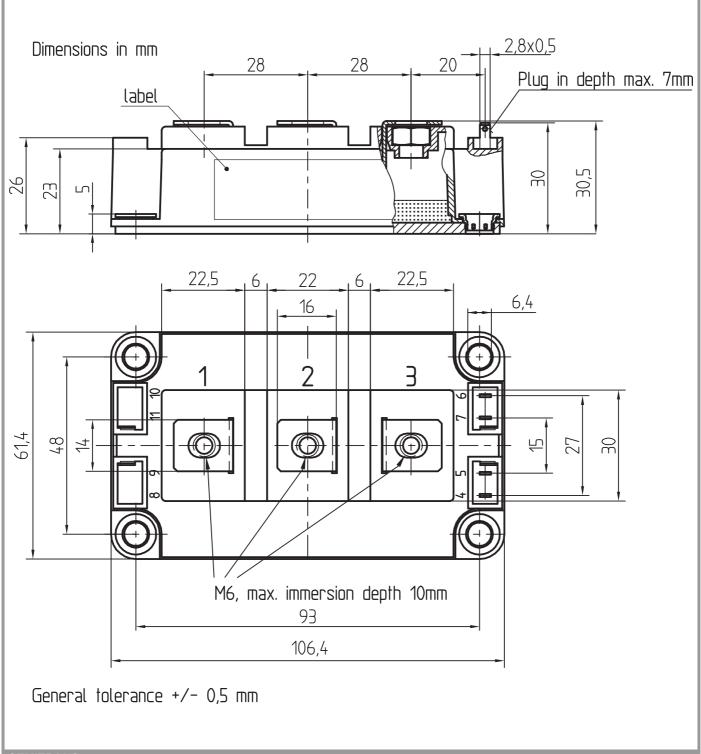


Fig. 8: Typ. switching times vs. gate resistor R_G

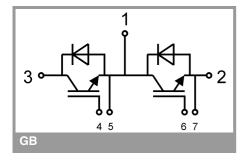












This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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